## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Gerhard KOTTSCHLAG

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For : VARACTOR DIODE ALTERNATIVE CIRCUIT

Examiner : John W. Poos

Group Art Unit : 2816 Confirmation No. : 8299

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Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria VA 22313-1450 I hereby certify that this correspondence is being electronically transmitted to the United States Patent and Trademark Office via the Office electronic filing system on **March 11, 2009**.

Signature: /Wendy Espinal/ Wendy Espinal

## **AMENDMENT AFTER A FINAL OFFICE ACTION**

SIR:

In response to the Final Office Action of February 2, 2009 (the three-month response date for which is May 2, 2009), please reconsider the above-identified application based on the following.

**Amendments to the Claims** are reflected in the listing of the claims which begins on page 2 of this paper.

**Remarks** begin on page 5 of this paper.

While no fee is believed to be due, the Commissioner is authorized to charge, as appropriate and/or necessary, any fees (including any Rule 136(a) extension fees) or credit any overpayment to Deposit Account No. 11-0600 of Kenyon & Kenyon LLP.